

Fig. 1 Time Delay Power Off

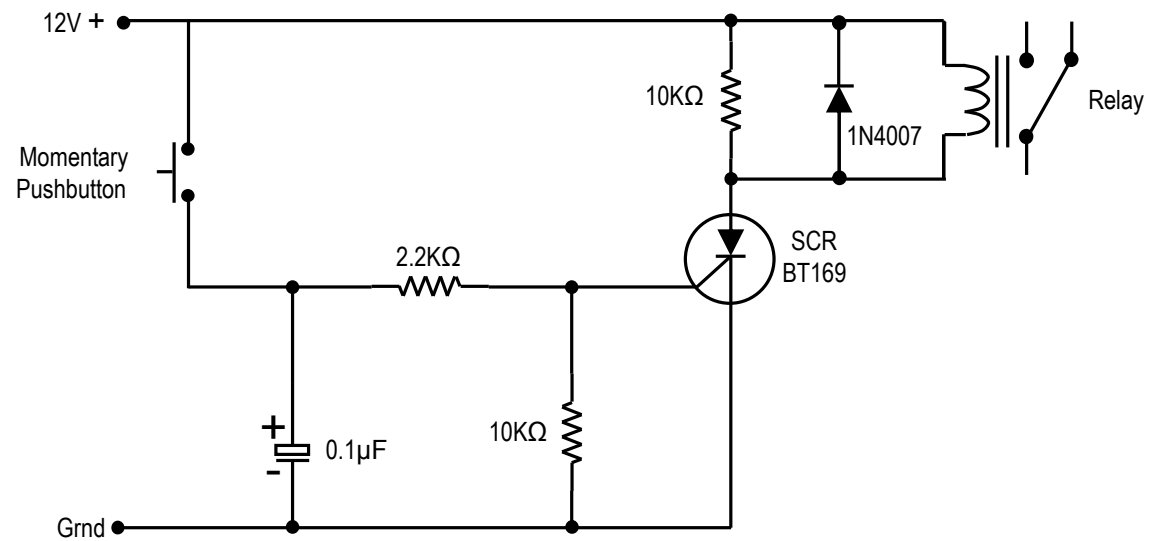


Fig. 2 SCR Relay Latch Driver Circuit