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RESET CIRCUIT FOR COM20051/COM20051+
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The power on reset circuit for the COM20051 and COM20051+ should be designed to provide a clean, fast transition time TTL input to the COM20051/COM20051+. Sufficient signal high time on RST (pin 10) should be provided after Vcc reaches +5V DC.

The following circuit, which provides an 8ms power-on reset pulse, is recommended: