

Pin Num.	Pin Name	Pin Type	I/O Struct.	Alternate Functions	Additional Functions
1	Vbat				
2	PC13	S		RTC_OUT/RTC_TAMP1/RTC_TS/EVENTOUT ????	Red LED
3	PC14	I/O	FT	EVENTOUT ????	OSC32_IN
4	PC15	I/O	FT	EVENTOUT ????	OSC32_OUT
5	PH0	I/O	FT	EVENTOUT ????	OSC_IN
6	PH1	I/O	FT	EVENTOUT ????	OSC_OUT
7	NRST		RST		HD1-P1 MCLR
8	PC0	I/O	FT	OTG_HS_ULPI_STP/EVENTOUT	ADC123_IN10 HD1-P6 AN4
9	PC1	I/O	FT	ETH_MDC/EVENTOUT	ADC123_IN11 HD1-P8 AN5
10	PC2	I/O	FT	SPI2_MISO/OTG_HS_ULPI_DIR/ETH_MII_TXD2/I2S2ext_SD/EVENTOUT	ADC123_IN12 HD1-P9 AN6
11	PC3	I/O	FT	SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/EVENTOUT	ADC123_IN13 Unconnected
12	Vssa	S			
13	Vdda	S			
14	PA0	I/O	FT	USART2_CTS/UART4_TX/ETH_MII_CRS/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/EVENTOUT	ADC123_IN0/WKUP HD1-P2 AN0
15	PA1	I/O	FT	USART2_RTS/UART4_RX/ETH_RMII_REF_CLK/ETH_MII_RX_CLK/TIM5_CH2/TIM2_CH2/EVENTOUT	ADC123_IN1 HD1-P3 AN1
16	PA2	I/O	FT	USART2_TX/TIM5_CH3/TIM9_CH1/TIM2_CH3/ETH_MDIO/EVENTOUT	ADC123_IN2 HD1-P4 AN2
17	PA3	I/O	FT	USART2_RX/TIM5_CH4/TIM9_CH2/TIM2_CH4/OTG_HS_ULPI_D0/ETH_MII_COL/EVENTOUT	ADC123_IN3 HD1-P5 AN3
18	Vss	S			
19	Vdd	S			
20	PA4	I/O	TTa	SPI1_NSS/SPI3_NSS/USART2_CK/DCMI_HSYNC/OTG_HS_SOF/I2S3_WS/EVENTOUT	ADC12_IN4/DAC_OUT1 HD2-P37 SPI0-SS
21	PA5	I/O	TTa	SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM8_CH1N/EVENTOUT	ADC12_IN5/DAC_OUT2 HD2-P38 SPI0-SCK
22	PA6	I/O	FT	SPI1_MISO/TIM8_BKIN/TIM13_CH1/DCMI_PIXCLK/TIM3_CH1/TIM1_BKIN/EVENTOUT	ADC12_IN6 HD2-P40 SPI0-MISO
23	PA7	I/O	FT	SPI1_MOSI/TIM8_CH1N/TIM14_CH1/TIM3_CH2/ETH_MII_RX_DV/	ADC12_IN7

Pin Num.	Pin Name	Pin Type	I/O Struct.	Alternate Functions	Additional Functions
				TIM1_CH1N/ETH_RMII_CRS_DV/EVENTOUT	HD2-P39 SPI0-MOSI
24	PC4	I/O	FT	ETH_RMII_RX_D0/ETH_MII_RX_D0/EVENTOUT	ADC12_IN14 HD2-P34 INT0
25	PC5	I/O	FT	ETH_RMII_RX_D1/ETH_MII_RX_D1/EVENTOUT	ADC12_IN15 HD2-P33 INT1
26	PB0	I/O	FT	TIM3_CH3/TIM8_CH2N/OTG_HS_ULPI_D1/ETH_MII_RXD2/TIM1_CH2N/EVENTOUT	ADC12_IN8 HD1-P19 PWM2
27	PB1	I/O	FT	TIM3_CH4/TIM8_CH3N/OTG_HS_ULPI_D2/ETH_MII_RXD3/TIM1_CH3N/EVENTOUT	ADC12_IN9 HD1-P20 PWM3
28	PB2	I/O	FT	EVENTOUT	BOOT1 Unconnected
29	PB10	I/O	FT	SPI2_SCK/I2S2_CK/I2C2_SCL/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/TIM2_CH3/EVENTOUT	HD2-P28 I2C-SCL
30	PB11	I/O	FT	I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_RMII_TX_EN/ETH_MII_TX_EN/TIM2_CH4/EVENTOUT	HD2-P27 I2C-SDA
31	Vcap1	S			
32	Vdd	S			
33	PB12	I/O	FT	SPI2_NSS/I2S2_WS/I2C2_SMBA/USART3_CK/TIM1_BKIN/CAN2_RX/OTG_HS_ULPI_D5/ETH_RMII_TXD0/ETH_MII_TXD0/OTG_HS_ID/EVENTOUT	HD1-P7 SPI1-SS
34	PB13	I/O	FT	SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N/CAN2_TX/OTG_HS_ULPI_D6/ETH_RMII_TXD1/ETH_MII_TXD1/EVENTOUT	OTG_HS_VBUS HD1-P18 SPI1-SCK
35	PB14	I/O	FT	SPI2_MISO/TIM1_CH2N/TIM12_CH1/OTG_HS_DM/USART3_RTS/TIM8_CH2N/I2S2ext_SD/EVENTOUT	HD2-P23 SPI1-MISO
36	PB15	I/O	FT	SPI2_MOSI/I2S2_SD/TIM1_CH3N/TIM8_CH3N/TIM12_CH2/OTG_HS_DP/EVENTOUT	RTC_REFIN HD2-P24 SPI1-MOSI
37	PC6	I/O	FT	I2S2_MCK/TIM8_CH1/SDIO_D6/USART6_TX/DCMI_D0/TIM3_CH1/EVENTOUT	Unconnected
38	PC7	I/O	FT	I2S3_MCK/TIM8_CH2/SDIO_D7/USART6_RX/DCMI_D1/TIM3_CH2/EVENTOUT	Unconnected
39	PC8	I/O	FT	TIM8_CH3/SDIO_D0/TIM3_CH3/USART6_CK/DCMI_D2/EVENTOUT	Unconnected
40	PC9	I/O	FT	I2S_CKIN/MCO2/TIM8_CH4/SDIO_D1/I2C3_SDA/DCMI_D3/TIM3_CH4/EVENTOUT	HD1-P15 INT3
41	PA8	I/O	FT	MCO1/USART1_CK/TIM1_CH1/I2C3_SCL/OTG_FS_SOF/EVENTOUT	HD1-P8 INT2

Pin Num.	Pin Name	Pin Type	I/O Struct.	Alternate Functions	Additional Functions
42	PA9	I/O	FT	USART1_TX/TIM1_CH2/I2C3_SMBA/DCMI_D0/EVENTOUT	USB-DET
43	PA10	I/O	FT	USART1_RX/TIM1_CH3/OTG_FS_ID/DCMI_D1/EVENTOUT	USB-ID
44	PA11	I/O	FT	USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM/EVENTOUT	USB-D_N
45	PA12	I/O	FT	USART1_RTS/CAN1_TX/TIM1_ETR/OTG_FS_DP/EVENTOUT	USB-D_P
46	PA13	I/O	FT	JTMS-SWDIO/EVENTOUT	HD2-P35 TMS/SWD
47	Vcap2	S			
48	Vdd	S			
49	PA14	I/O	FT	JTCK-SWCLK/EVENTOUT	HD2-P36 TCK/SWC
50	PA15	I/O	FT	JTDI/ SPI3_NSS/I2S3_WS/TIM2_CH1_ETR/SPI1_NSS/EVENTOUT	HD2-P30 TDI
51	PC10	I/O	FT	SPI3_SCK/I2S3_CK/UART4_TX/SDIO_D2/DCMI_D8/USART3_TX/EVENTOUT	HD2-P21 U1TX
52	PC11	I/O	FT	UART4_RX/SPI3_MISO/SDIO_D3/DCMI_D4/USART3_RX/I2S3ext_SD/EVENTOUT	HD2-P22 U1RX
53	PC12	I/O	FT	UART5_TX/SDIO_CK/DCMI_D9/SPI3_MOSI/I2S3_SD/USART3_CK/EVENTOUT	Yellow LED
54	PD2	I/O	FT	TIM3_ETR/UART5_RX/SDIO_CMD/DCMI_D11/EVENTOUT	Unconnected
55	PB3	I/O	FT	JTDO/TRACESWO/SPI3_SCK/I2S3_CK/TIM2_CH2/SPI1_SCK/EVENTOUT	HD2-P29 TDO
56	PB4	I/O	FT	NJTRST/SPI3_MISO/TIM3_CH1/SPI1_MISO/I2S3ext_SD/EVENTOUT	HD1-P16 PWM0
57	PB5	I/O	FT	I2C1_SMBA/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/TIM3_CH2/SPI1_MOSI/SPI3_MOSI/DCMI_D10/I2S3_SD/EVENTOUT	HD1-P17 PWM1
58	PB6	I/O	FT	I2C1_SCL/TIM4_CH1/CAN2_TX/DCMI_D5/USART1_TX/EVENTOUT	HD2-P25 U0TX
59	PB7	I/O	FT	I2C1_SDA/FSMC_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2/EVENTOUT	HD2-P26 U0RX
60	BOOT0	I	B		Vpp
61	PB8	I/O	FT	TIM4_CH3/SDIO_D4/TIM10_CH1/DCMI_D6/ETH_MII_TXD3/I2C1_SCL/CAN1_RX/EVENTOUT	HD1-P14 CRX
62	PB9	I/O	FT	SPI2_NSS/I2S2_WS/TIM4_CH4/TIM11_CH1/SDIO_D5/DCMI_D7/I2C1_SDA/CAN1_TX/EVENTOUT	HD1-P13 CTX
63	Vss	S			
64	Vdd	S			

S	Supply pin
I	Input only pin
I/O	Input/output pin
FT	5V tolerant pin
TTa	3.3V tolerant I/O directly connected to ADC
B	Dedicated BOOT0 pin
RST	Bidirectional reset pin with embedded weak pull-up resistor