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## Intersil

## ICL7106 - 3 1/2 Digit LCD Display, A/D Converter

## Photograph



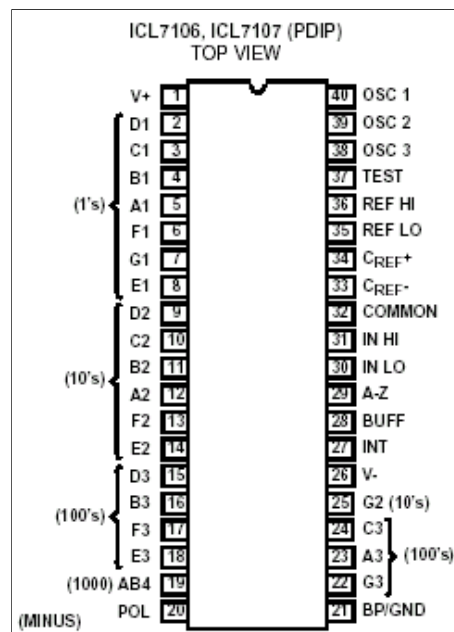
## Features

- ▶ Guaranteed Zero Reading for 0V Input on All Scales
- ▶ True Polarity at Zero for Precise Null Detection
- ▶ 1pA Typical Input Current
- ▶ True Differential Input and Reference, Direct Display Drive
- ▶ Low Noise
- ▶ On Chip Clock and Reference
- ▶ Low Power Dissipation
- ▶ No Additional Active Circuits Required
- ▶ Enhanced Display Stability

## Pin Description

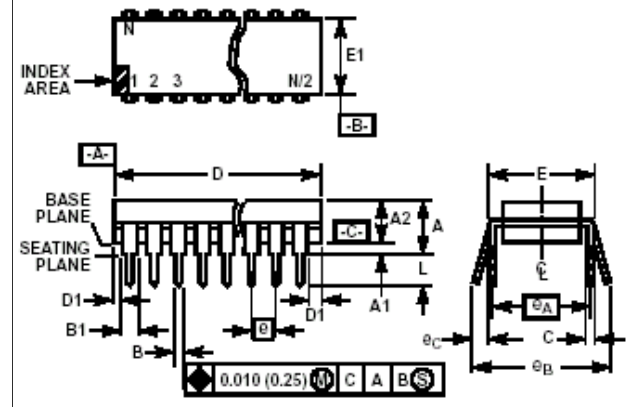
Pin Number	Description
1	V+ - Positive Supply
2	D1 - 1's Digit - D Segment
3	C1 - 1's Digit - C Segment
4	B1 - 1's Digit - B Segment
5	A1 - 1's Digit - A Segment
6	F1 - 1's Digit - F Segment
7	G1 - 1's Digit - G Segment
8	E1 - 1's Digit - E Segment
9	D2 - 10's Digit - D Segment
10	C2 - 10's Digit - C Segment
11	B2 - 10's Digit - B Segment
12	A2 - 10's Digit - A Segment

## Pin Layout



## Dimensional Drawing

13	F2 - 10's Digit - F Segment
14	E2 - 10's Digit - E Segment
15	D3 - 100's Digit - D Segment
16	B3 - 100's Digit - B Segment
17	F3 - 100's Digit - F Segment
18	E3 - 100's Digit - E Segment
19	AB4 - 1000's Digit
20	POL - Polarity Sign
21	BP/GND - Backplane Conn. for LCD/Ground
22	G3 - 100's Digit - G Segment
23	A3 - 100's Digit - A Segment
24	C3 - 100's Digit - C Segment
25	G2 - 10's Digit - G Segment
26	Vneg - Negative Supply
27	INT - Integrator
28	BUFF - Buffer
29	A-Z - Auto Zero
30	IN LO - Input Low
31	IN HI - Input High
32	COMMON - Input Common
33	Cref- - Reference Capacitor
34	Cref+ - Reference Capacitor
35	REF LO - Reference Low
36	REF HI - Reference High
37	TEST - Test
38	OSC3 - Oscillator
39	OSC2 - Oscillator



**E40.6** (JEDEC MS-011-AC ISSUE B)  
40 LEAD DUAL-IN-LINE PLASTIC PACKAGE

SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN	MAX	MIN	MAX	
A	-	0.250	-	6.35	4
A1	0.015	-	0.39	-	4
A2	0.125	0.195	3.18	4.95	-
B	0.014	0.022	0.356	0.558	-
B1	0.030	0.070	0.77	1.77	8
C	0.008	0.015	0.204	0.381	-
D	1.980	2.095	50.3	53.2	5
D1	0.005	-	0.13	-	5
E	0.600	0.625	15.24	15.87	6
E1	0.485	0.580	12.32	14.73	5
e	0.100 BSC		2.54 BSC		-
eA	0.600 BSC		15.24 BSC		6
eB	-	0.700	-	17.78	7
L	0.115	0.200	2.93	5.08	4
N	40		40		9

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OSC1 - Oscillator

**Technical Data****Data Sheet**

Intersil - Data Sheet for ICL7106

**Application Notes**

The Integrating A/D Convertor



Low Cost Digital Panel Meter Designs and Complete Instructions for LCD and LED Kits



Building A Battery Operated Auto Ranging DVM With the ICL7106



Tips For Using Single Chip 3 1/2 Digit A/D Converters



Overcoming Common Mode Range Issues When Using Intersil Integrating Converters

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