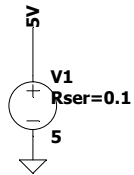


wave 0-5V

V2
Rser=0.1
PULSE(0 5 0 50n 50n 10m 20m)

U25
LT1006

5V



The circuit diagram illustrates a 4-bit parallel adder. It features a 5V supply connected to a switch (SW) and a 1.5V source (A1). The circuit includes two 2N3904 transistors (Q1, Q2) and two 2N3906 transistors (Q3, Q4). Diodes 1N4148 (D1, D2, D3, D4) are used for signal processing. Resistors R1 through R10 are specified with values: R1 (5k1), R2 (5k1), R3 (10k), R4 (10k), R5 (10k), R6 (10k), R7 (10k), R8 (10k), R9 (10K), and R10 (1K). The output is labeled OUT.