



SPECIFICATIONS FOR LCD MODULE

CUSTOMER	STD
MODEL	WM-G3224Y-1NFWe VER. 1
CUSTOMER APPROVED	

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APPROVAL FOR SPECIFICATIONS AND SAMPLE

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Reference Data:
Novatek NT7702-T4,NT7701H Data Sheet

(1) Electronic Units

1.1 Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
OPERATING TEMPERATURE	T_{OP}	0	-	+50	°C
STORAGE TEMPERATURE	T_{ST}	-20	-	+70	°C
INPUT VOLTAGE	V_I	-0.3	-	$V_{DD}+0.3$	V
SUPPLY VOLTAGE FOR LOGIC	$V_{DD}-V_{SS}$	-0.3	-	+7.0	V
SUPPLY VOLTAGE FOR LCD	$V_{EE}-V_{SS}$	-0.3	-	+30.0	V
STATIC ELECTRICITY	Be sure that you are grounded when handing LCM.				

1.2 Electrical Characteristics

($T_a=25^{\circ}\text{C}$, $V_{DD}=3.3\text{V}$)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
SUPPLY VOLTAGE FOR LOGIC	$V_{DD}-V_{SS}$	-	3.0	3.3	3.6	V
SUPPLY VOLTAGE FOR LCD	$V_{EE}-V_{SS}$ (V_{LCD})	-	21.5	22.5	23.5	V
INPUT HIGH VOL.	V_{IH}	-	$0.8V_{DD}$	-	V_{DD}	V
INPUT LOW VOL.	V_{IL}	-	0	-	$0.2V_{DD}$	V
OUTPUT HIGH VOL.	V_{OH}	$I_{OH}=-0.4\text{mA}$	$V_{DD}-0.4$	-	-	V
OUTPUT LOW VOL.	V_{OL}	$I_{OL}=0.4\text{mA}$	-	-	0.4	V
SUPPLY CURRENT FOR LOGIC	$*I_{DD}$	FLM=75 Hz	-	0.13	0.20	mA
SUPPLY CURRENT FOR LCD	$*I_{EE}$	$V_{EE}=22.5\text{V}$ FLM=75 Hz	-	4.29	6.43	mA
FRAME FREQUENCY	FLM	-	70	-	85	Hz
USED IC	NT7701H & NT7702-T4					

* I_{DD} Measurement condition is for all pixels on display

* I_{EE} Measurement condition is for all pixels on display

1.3 Interface Pin Function

CN1:

NO	SYMBOL	I / O	FUNCTION
1.	FLM	I/O	The FLM signal indicate the beginning of each display circle
2.	LP	I/O	Input latch signal
3.	CP	I/O	Data input clock signal
4.	/DISPOFF	I/O	Display control signal
5.	VDD	P	Power supply for logic
6.	VSS	P	Ground
7.	VEE	P	Power supply for LCD
8.	NC	-	No connection
9.	NC	-	No connection
10.	NC	-	No connection
11.	NC	-	No connection
12.	DB3	I	Data bus
13.	DB2	I	
14.	DB1	I	
15.	DB0	I	

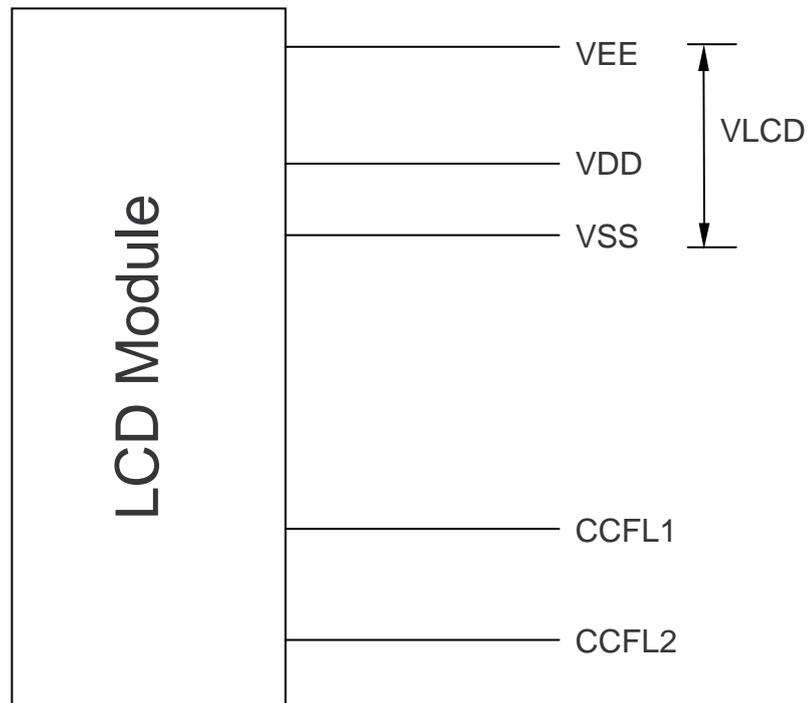
CN2:

1.	CCFL1	P	Power supply for CCFL
2.	NC	-	No connection
3.	CCFL2	P	Power supply for CCFL

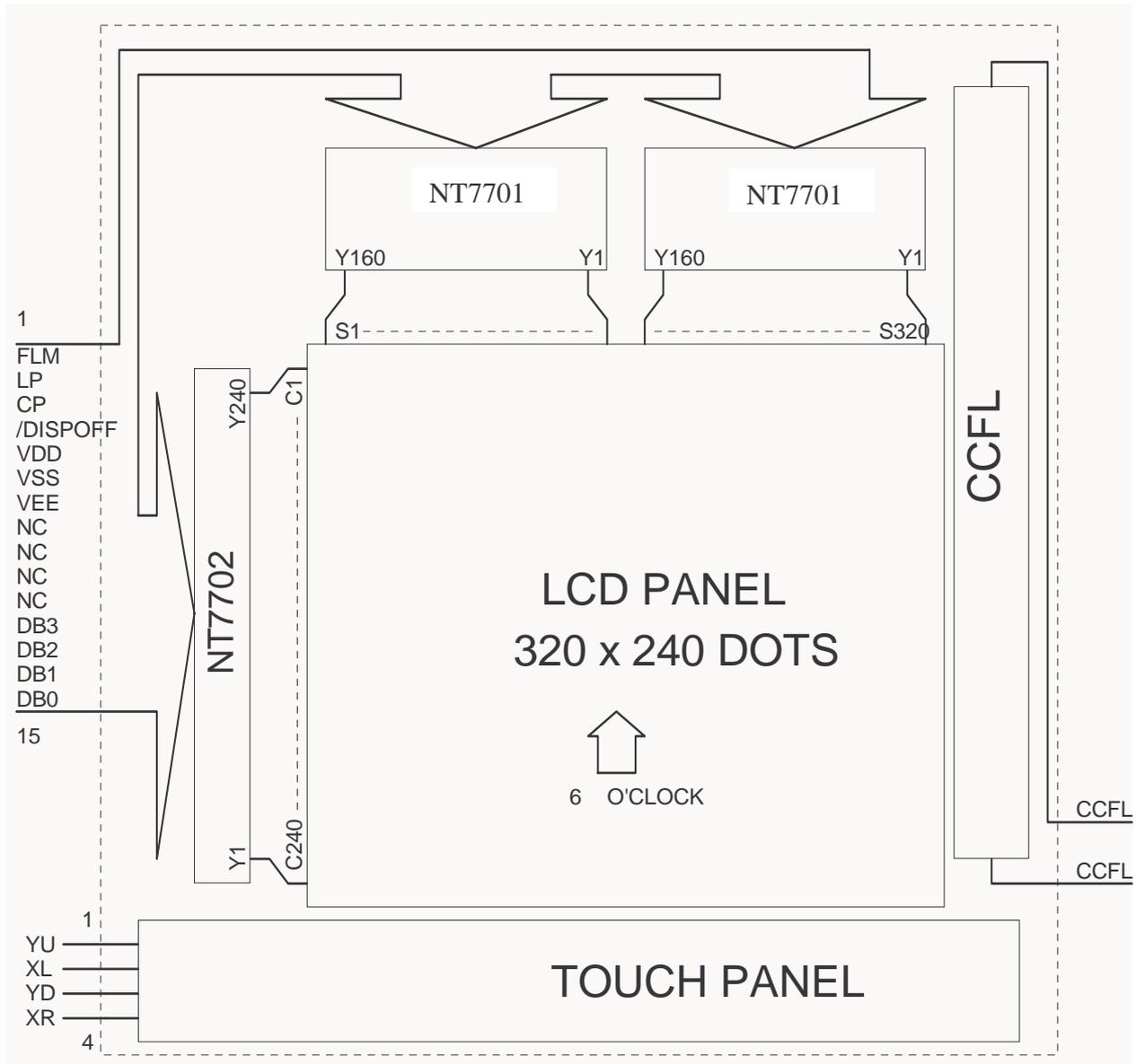
CN3:

1	YU	-	Touch Panel Pin Output Upper (Y Axis)
2	XL	-	Touch Panel Pin Output Left (X Axis)
3	YD	-	Touch Panel Pin Output Lower (Y Axis)
4	XR	-	Touch Panel Pin Output Right (X Axis)

1.4 Power Supply for LCD Module



1.5 Block Diagram



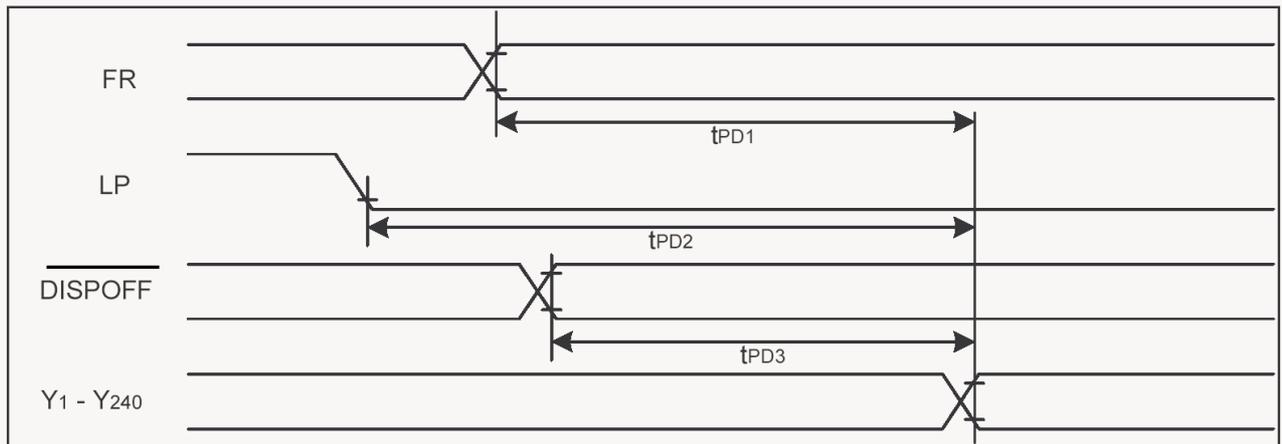
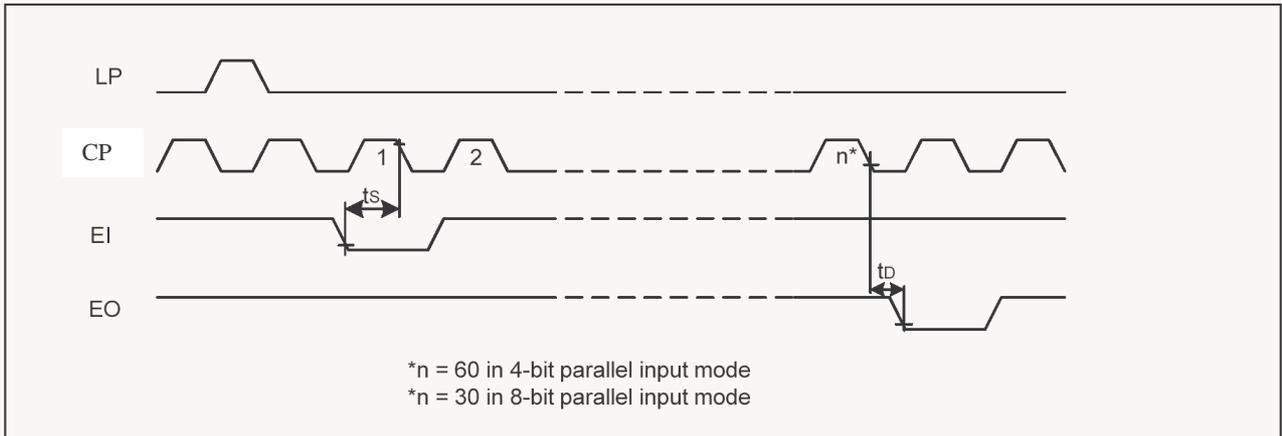
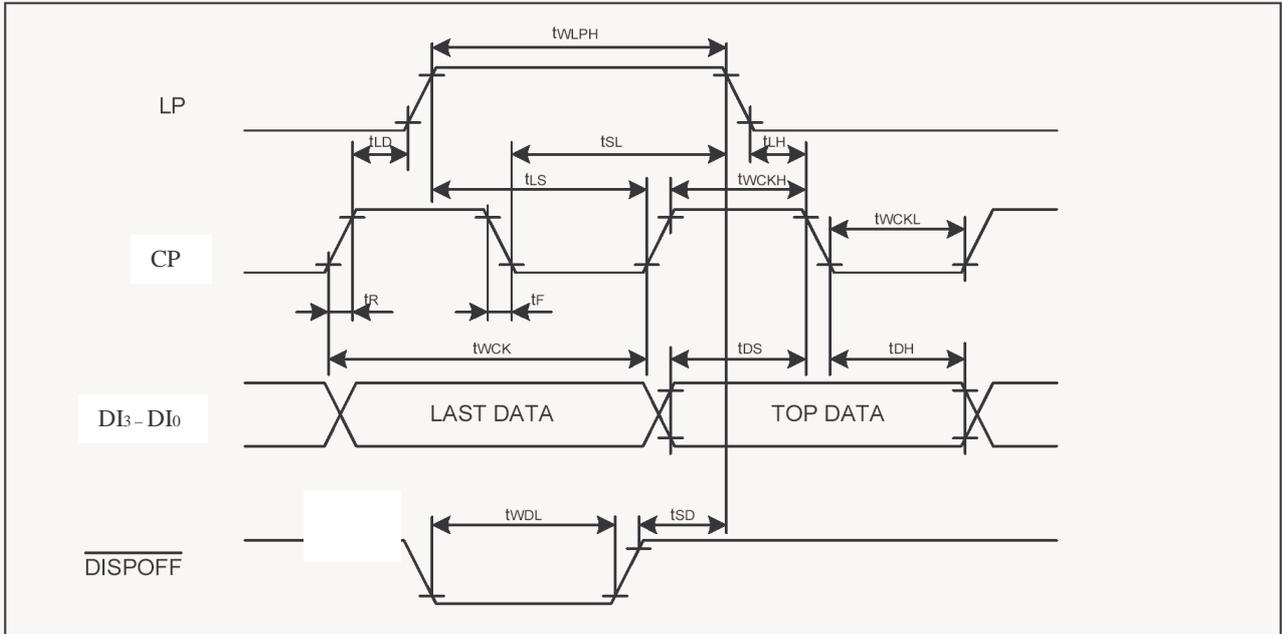
1.6 Timing Characteristic

(Segment Mode 2) ($V_{SS} = V_5 = 0\text{ V}$, $V_{DD} = +3.0\text{ to }+4.5\text{ V}$, $V_0 = +15.0\text{ to }+32.0\text{ V}$, $T_{OPR} = -20\text{ to }+85\text{ }^\circ\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Shift clock period	t_{WCK}	$t_r, t_f \leq 10\text{ns}$	66			ns	1
Shift clock "H" pulse width	t_{WCKH}		23			ns	
Shift clock "12" pulse width	t_{WCKL}		23			ns	
Data setup time	t_{DS}		15			ns	
Data hold time	t_{DH}		23			ns	
Latch pulse "H" pulse width	t_{WLPH}		30			ns	
Shift clock rise to latch pulse rise time	t_{LD}		0			ns	
Shift clock fall to latch pulse fall time	t_{SL}		50			ns	
Latch pulse rise to shift clock rise time	t_{LS}		30			ns	
Latch pulse fall to shift clock fall time	t_{LH}		30			ns	
Enable setup time	t_S		15			ns	
Input signal rise time	t_R				50	ns	2
Input signal fall time	t_F				50	ns	2
DISPOFF removal time	t_{SD}		100			ns	
DISPOFF "L" pulse width	t_{WDL}		1.2			μs	
Output delay time (1)	t_D	$CL = 15\text{ pF}$			41	ns	
Output delay time (2)	T_{PD1}, t_{PD2}	$CL = 15\text{ pF}$			1.2	μs	
Output delay time (3)	t_{PD3}	$CL = 15\text{ pF}$			1.2	μs	

NOTES:

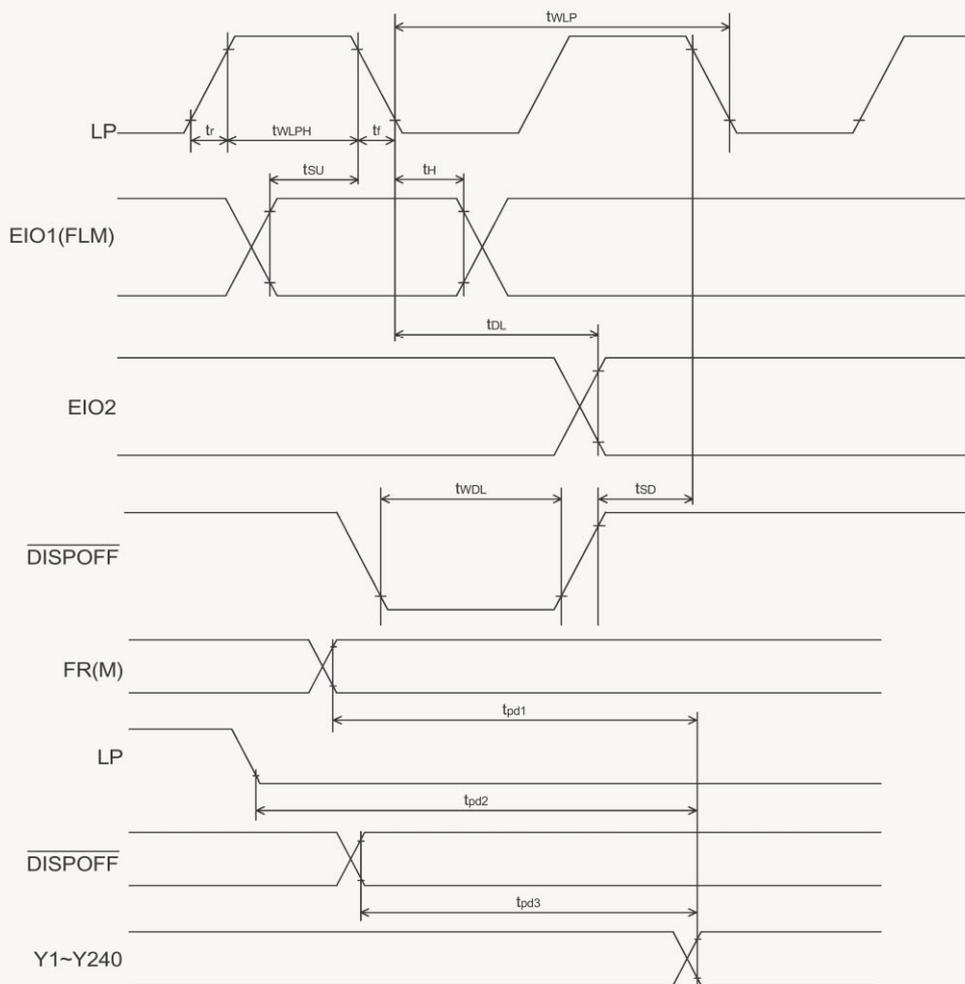
1. Takes the cascade connection into consideration.
2. $(t_{WCK} - t_{WCKH} - t_{WCKL})/2$ is maximum in the case of high speed operation.



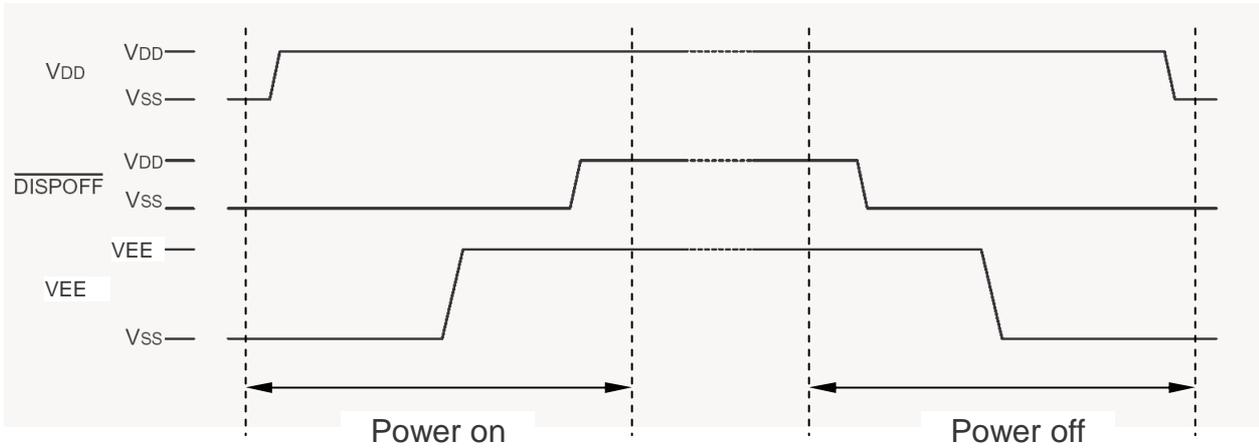
Common Mode ($V_{SS}=V_5=0V$, $V_{DD}= 2.5\sim 5.5V$, $V_0=15$ to $30 V$ and $T_A=-20$ to $+85^\circ C$, unless otherwise noted.)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Shift clock period	tWLP	250	-	-	ns	$t_r, t_f \leq 20ns$
Shift clock "H" pulse width	tWLPH	15	-	-	ns	$V_{DD}=+5.0V \pm 10\%$
		30	-	-	ns	$V_{DD}=+2.5\sim +4.5V$
Data setup time	tSU	30	-	-	ns	
Data hole time	tH	50	-	-	ns	
Input signal rise time	t _r		-	50	ns	
Input signal fall time	t _f		-	50	ns	
$\overline{DISPOFF}$ Removal time	tSD	100	-	-	ns	
$\overline{DISPOFF}$ enable pulse width	tWDL	1.2	-	-	μs	
Output delay time (1)	tDL	-	-	200	ns	$C_L=15pF$
Output delay time (2)	t _{pd1} , t _{pd2}	-	-	1.2	μs	$C_L=15pF$
Output delay time (3)	t _{pd3}	-	-	1.2	μs	$C_L=15pF$

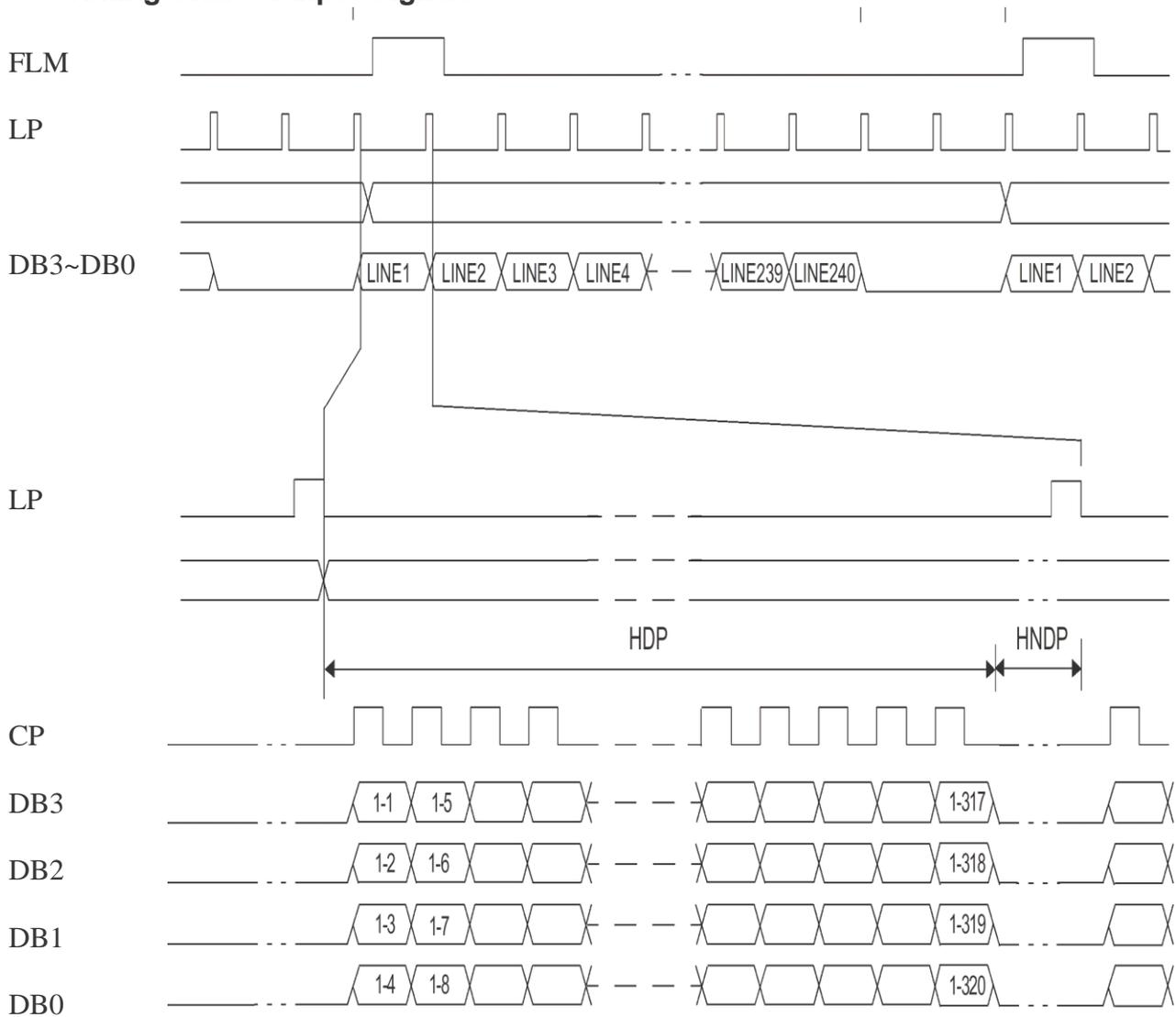
Timing Characteristics of Common Mode



Timing Characteristics of Power On and Power Off



Timing Chart of Input Signals

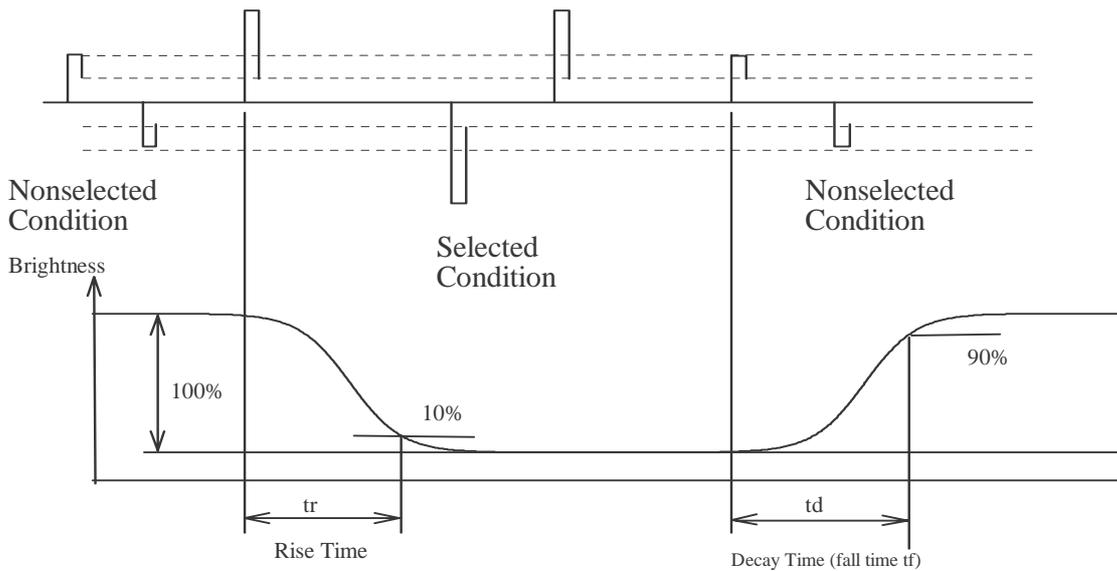


(2) Electro-optical Units

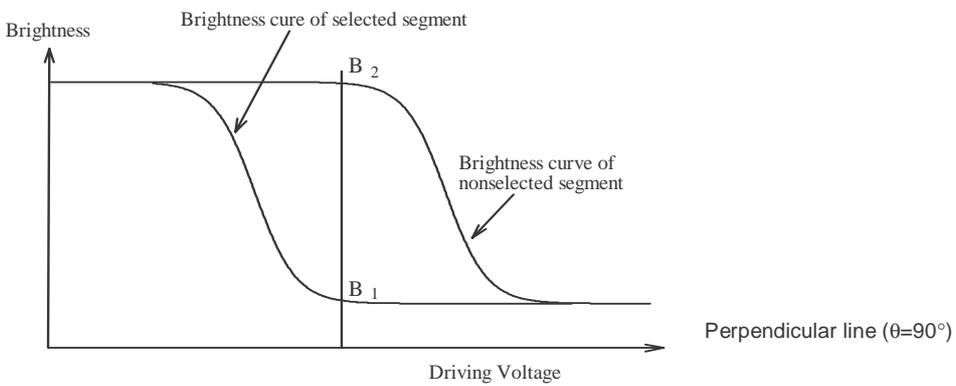
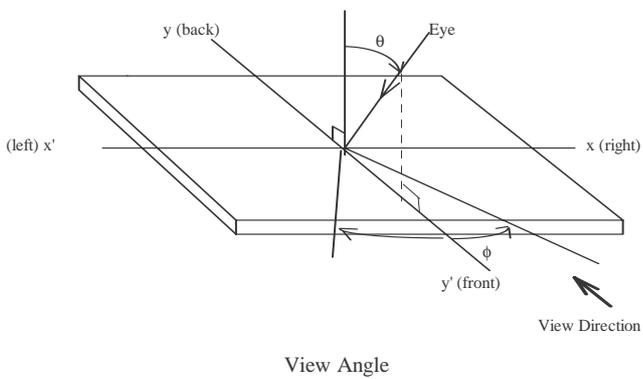
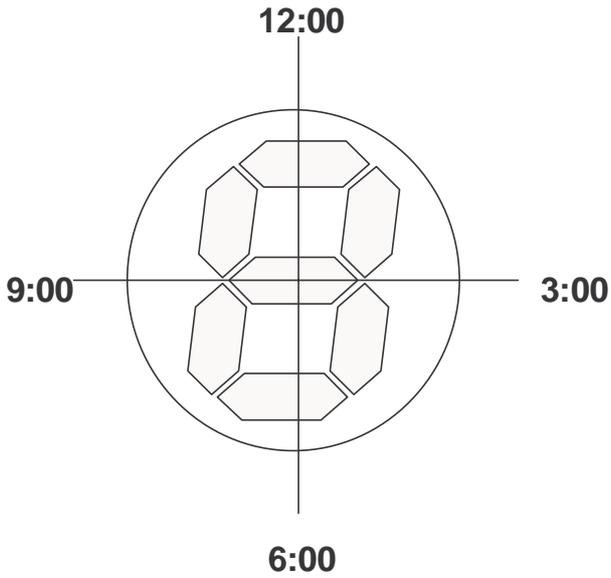
2.1 Electro-optical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
VIEW ANGLE (V)	θ	$CR \geq 2$	-40	-	+40	deg.
VIEW ANGLE (H)	ψ	$CR \geq 2$	-40	-	+40	deg.
CONTRAST RATIO	CR	$T_a=25^\circ\text{C}$	-	15	-	-
RESPONSE TIME	t_r	$T_a=25^\circ\text{C}$	-	200	350	ms
RESPONSE TIME	t_d	$T_a=25^\circ\text{C}$	-	200	350	ms
OPERATING VOLTAGE FOR LCD	V_{LCD}	$T_a=25^\circ\text{C}$	-	22.5	-	V
DRIVE METHOD	DUTY	1/240				
	BIAS	1/13				
LCD TYPE	FSTN (Negative / Transmissive)					
VIEWING DIRECTION	6 O'CLOCK					

2.2 Optical Definitions



Response Time



$$\text{Contrast ratio} = \frac{\text{Brightness at nonselected segment}}{\text{Brightness at selected segment}}$$

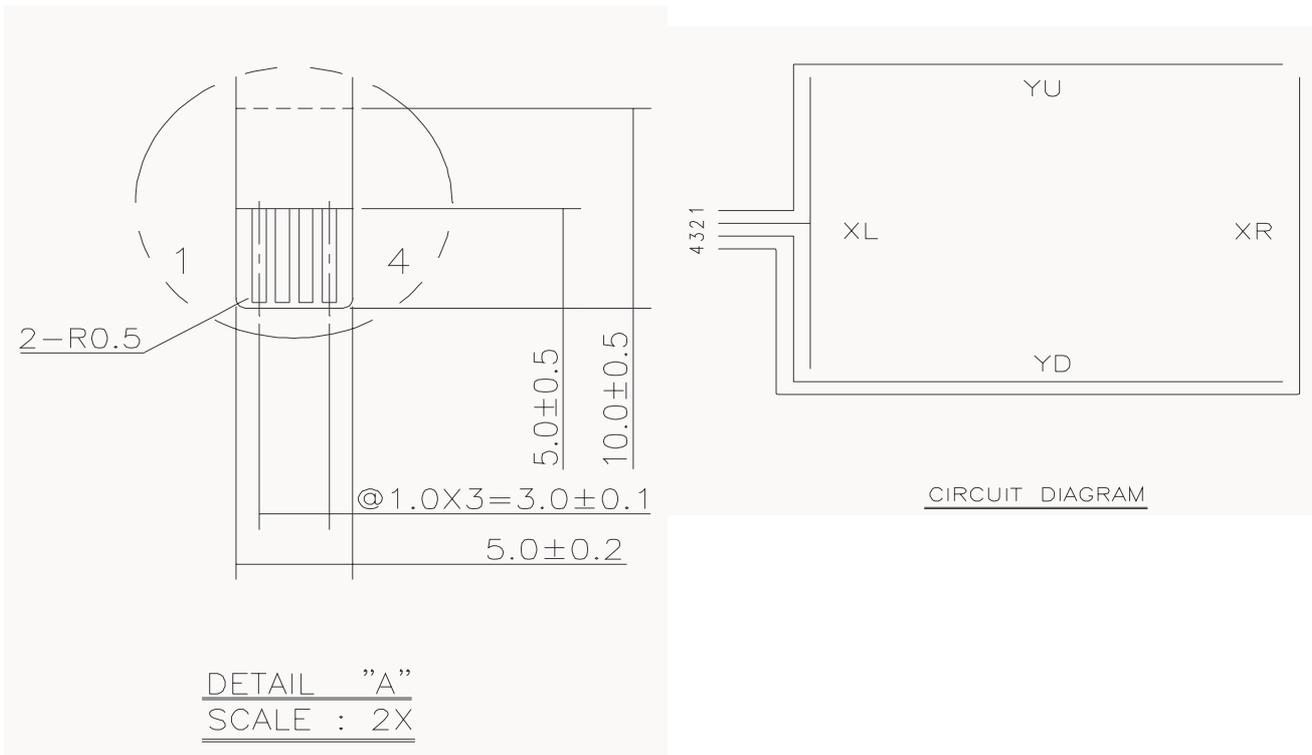
Contrast ratio (CR)

2.3 Touch Panel Specification

Electrical Characteristics:

Transmission:	JIS-K7361, JIS-K7105	80 or more	%
On Load	Pen Input	10~80	g
	Finger Input	20g Min	g
Resistance Between Leads	X axis	200~900	Ω
	Y axis	200~900	Ω
Insulation Resistance	25V DC	20	M Ω
Knocking Life	250g, 5Hz	1000000	Times

Outline Dimension:

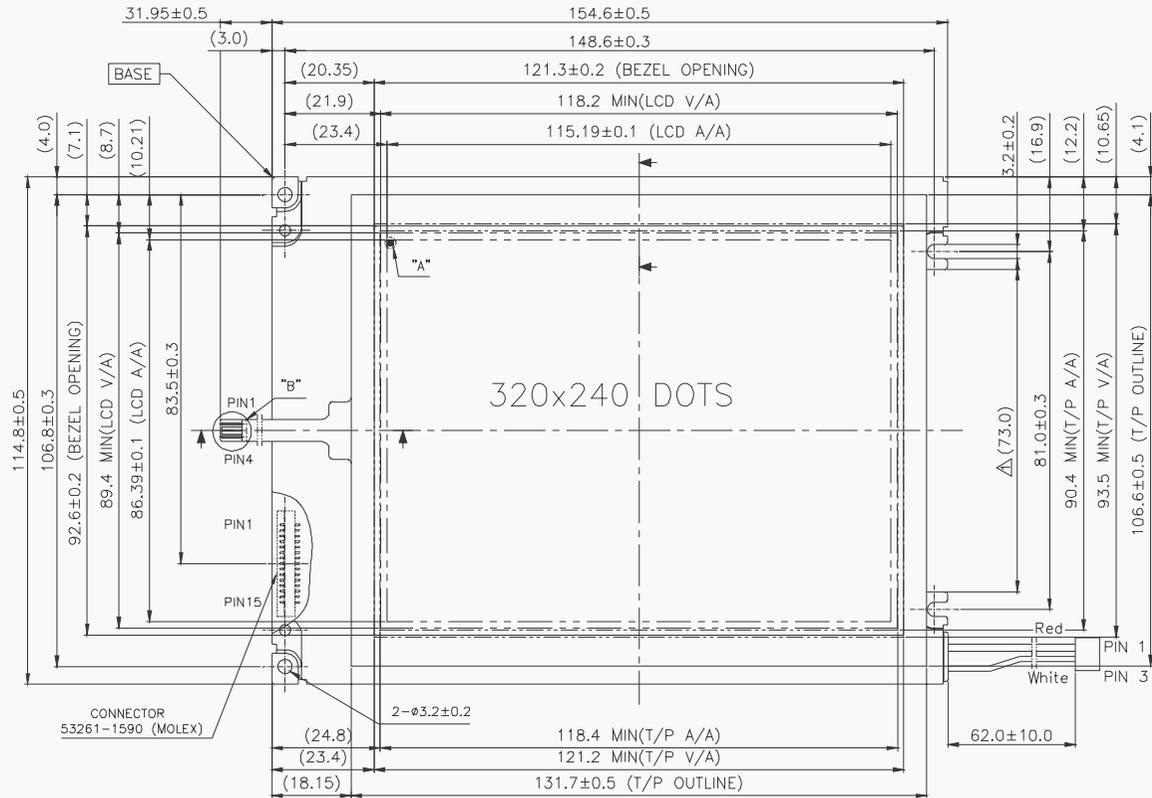


(3) Mechanical Units

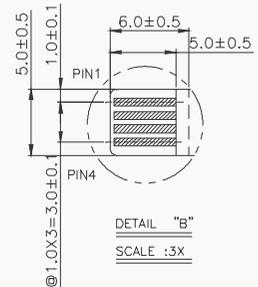
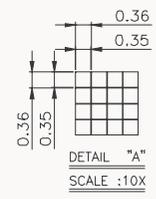
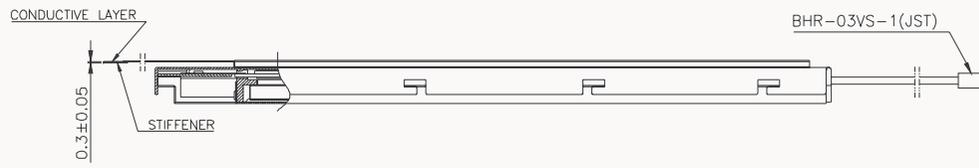
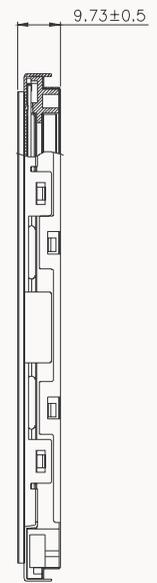
3.1 Mechanical Specification

ITEM	STANDARD VALUE	UNIT
NUMBER OF DOTS	320 × 240	dots
MODULE DIMENSION	154.6 (W) × 114.8(H) × 9.73 (T)	mm
VIEWING AREA	118.2 MIN. (W) × 89.4 MIN. (H)	mm
ACTIVE AREA	115.19 (W) × 86.39 (H)	mm
DOT SIZE	0.35 (W) × 0.35 (H)	mm
DOT PITCH	0.36(W) × 0.36 (H)	mm
APPROX. WEIGHT	220	g
BACK LIGHT	CCFL (COOL-WHITE)	

3.2 Mechanical Diagram



VIEWING DIRECTION



3.3 Back-light Specification

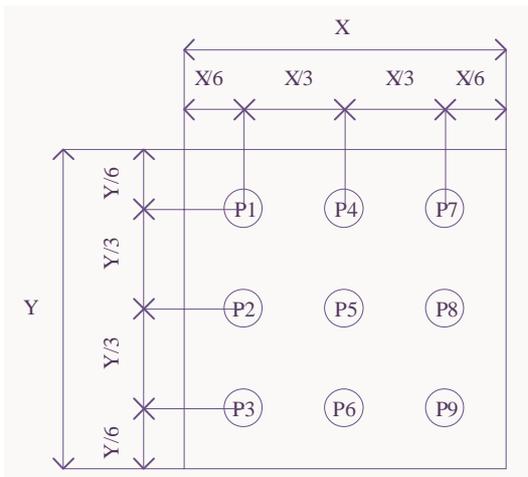
The CCFL backlight is distributed over the whole light area of the illumination unit, which gives the most uniform light.

3.3.1 Data About CCFL Backlight : (Test Environment : 25(C 60%RH)

PARAMENT	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Driving Voltage	VL	-	400	-	Vrms	-
Lamp Frequency	FL	-	70	80	KHz	-
Lamp Current	IL	4	4.5	5.5	mArms	-
Luminous Intensity	-	150	180	-	cd/m ²	Note 1,3
Luminous intensity Ratio	-	-	20	-	%	Note 2
Lamp Rise Time	T _s	-	5	-	Min	Reference
Life Time	-	15000	25000	-	Hrs	-

- Note :
- 1.Average Luminous Intensity of P1 to P9
 - 2.Luminous intensity Ratio =((Max-Min)/Max)×100%
 - 3.Recommend inverter (Wintek WM-FL003)
 - 3.1 Mesurement circuit current : 554mA(MAX)
 - circuit voltage : DC5.0V(TYP), 5.5V(MAX)

3.3.2 Measured Method:(X*Y:Light Area)



(Effective spatial Distribution)
 Hole Diameter ϕ 3mm : 1 to 9 per Position
 Measured Luminous Intensity

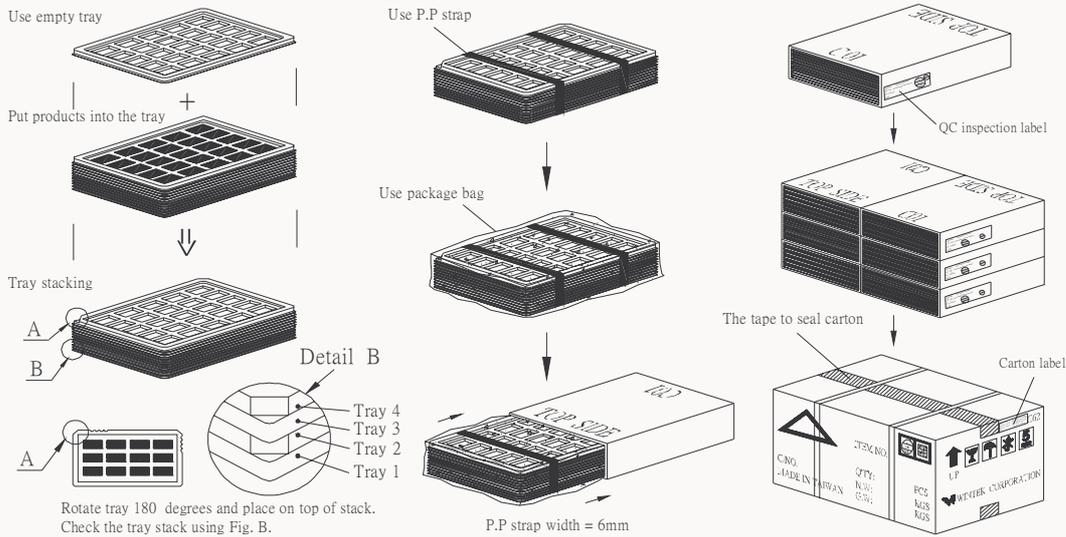
3.4 Packing Method

1. Packaging Material : (per carton)

NO.	Item	Model	Dimensions (mm)	Unit Weight (Kg)	Quantity	
1	LCM Module	WM-G3224Y-1NFWe	154.6*114.8	0.2192	48	
2	Tray	V242	PETA	320*217*16*0.6	0.06	30
3	Product Box	C01	320*219*70	0.131	6	
4	Carton	C62	475*345*250	0.857	1	
5	Package Bag	C5	467*321*0.08	0.023	6	
6	Total Weight	14.1	Kg± 5%			

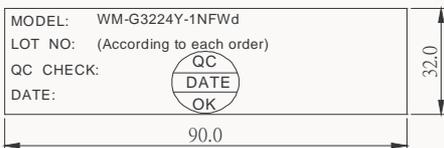
2. Packaging Specifications and Quantity :

- (1) LCM quantity per tray : quantity per row 1 x quantity per column 2 = 2
- (2) LCM quantity per box : quantity per tray 2 x quantity of trays 4 = 8
- (3) Total LCM quantity in carton : quantity per box 8 x quantity of boxes 6 = 48



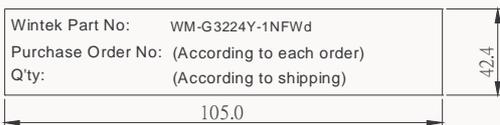
3. Label Specifications :

(1) QC Inspection Label



Label Color----Green

(2) Carton Label



Label Color----White

Remark

(4) Quality Units

4.1 Specification of Quality Assurance

4.1-1.Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by WINTEK CORPORATION (Supplier).

4.1-2.Standard for Quality Test

a. Inspection :

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to **ANSI/ASQC Z1.4-1993.General Inspection Level II take a single time.**

(ii) The defects classify of AQL as following:

Major defect: AQL=0.65

Minor defect: AQL=2.5

Total defects: AQL=2.5

4.1-3.Nonconforming Analysis & Deal With Manners

a. Nonconforming analysis:

(i) Purchaser should supply the detail data of non-conforming sample and the non-suitable state.

(ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.

(iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.

b. Disposition of nonconforming:

(i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.

(ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

4.1-4. Agreement items

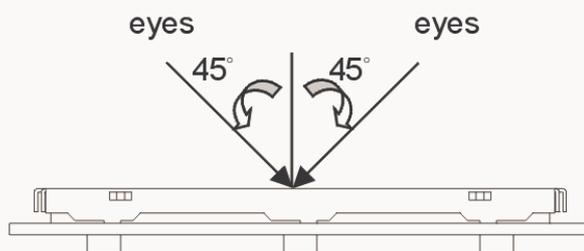
Both sides should discuss together when the following problems happen.

- a. There is any problem of standard of quality assurance, and both sides think that it must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.

4.1-5. Standard of The Product Appearance Test

a. Manner of appearance test:

- (i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30 cm.
- (ii) When test the model of transmissive product must add the reflective plate.
- (iii) The test direction is base on about around 45° of vertical line.

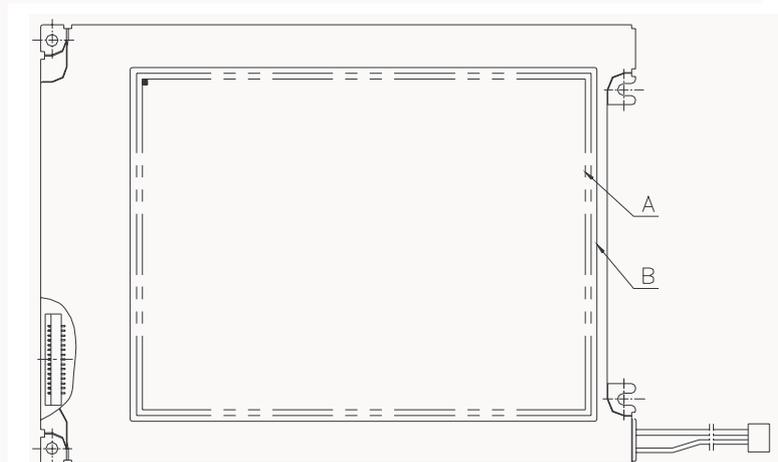


(iv) Definition of area:

A Area : Viewing area.

B Area : Out of viewing area.

(Outside viewing area)

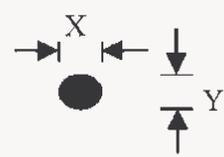
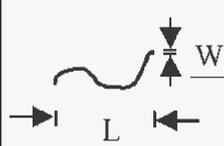


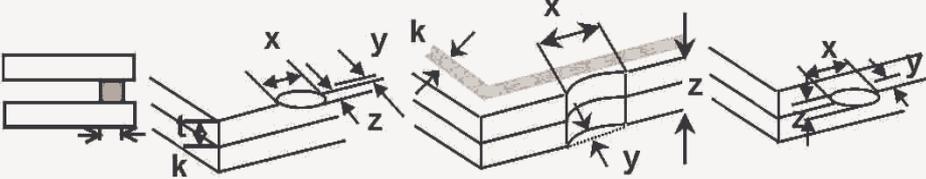
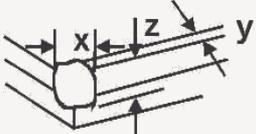
b. Basic principle:

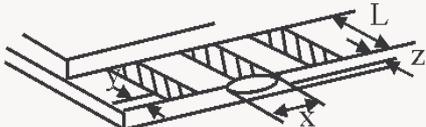
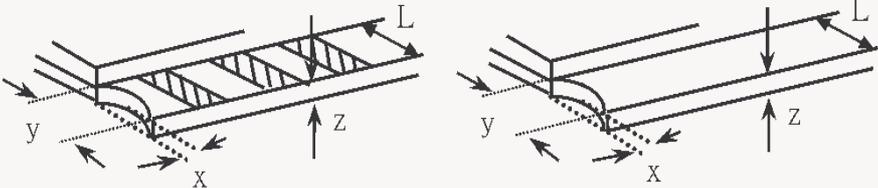
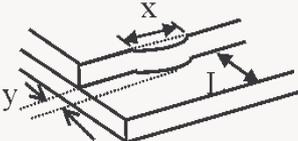
- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.

c. Standard of inspection:(Unit: mm)

4.1-6. Inspection specification

NO	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65												
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm.	2.5												
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\phi = (x + y) / 2$  <table border="1" data-bbox="734 1075 1308 1355"> <thead> <tr> <th>SIZE</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \phi \leq 0.20$</td> <td>2</td> </tr> <tr> <td>$0.20 < \phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \phi$</td> <td>0</td> </tr> </tbody> </table>	SIZE	Acceptable Q TY	$\phi \leq 0.10$	Accept no dense	$0.10 < \phi \leq 0.20$	2	$0.20 < \phi \leq 0.25$	1	$0.25 < \phi$	0	2.5		
		SIZE	Acceptable Q TY												
$\phi \leq 0.10$	Accept no dense														
$0.10 < \phi \leq 0.20$	2														
$0.20 < \phi \leq 0.25$	1														
$0.25 < \phi$	0														
3.2 Line type : (As following drawing)  <table border="1" data-bbox="670 1478 1340 1758"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>--</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.02 < W \leq 0.03$</td> <td rowspan="2">2</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.03 < W \leq 0.05$</td> </tr> <tr> <td>--</td> <td>$0.05 < W$</td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable Q TY	--	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	--	$0.05 < W$	As round type	2.5
Length	Width	Acceptable Q TY													
--	$W \leq 0.02$	Accept no dense													
$L \leq 3.0$	$0.02 < W \leq 0.03$	2													
$L \leq 2.5$	$0.03 < W \leq 0.05$														
--	$0.05 < W$	As round type													

NO	Item	Criterion	AQL																		
04	Polarizer bubbles	<p>If bubbles are visible, judge using black spot specifications ,not easy to find, must check in specify direction</p> <table border="1" data-bbox="778 365 1321 667"> <thead> <tr> <th>Size ϕ</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.20$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.20 < \phi \leq 0.50$</td> <td>3</td> </tr> <tr> <td>$0.50 < \phi \leq 1.00$</td> <td>2</td> </tr> <tr> <td>$1.00 < \phi$</td> <td>0</td> </tr> <tr> <td>Total Q TY</td> <td>3</td> </tr> </tbody> </table>	Size ϕ	Acceptable Q TY	$\phi \leq 0.20$	Accept no dense	$0.20 < \phi \leq 0.50$	3	$0.50 < \phi \leq 1.00$	2	$1.00 < \phi$	0	Total Q TY	3	2.5						
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05	Scratches	Follow NO.3 LCD black spots, white spots, contamination																			
06	Chipped glass	<p>Symbols : x : Chip length y : Chip width z : Chip thickness k : Seal width t : Glass thickness a : LCD side length L : Electrode pad length</p> <p>6.1 General glass chip :</p> <p>6.1.1 Chip on panel surface and crack between panels :</p>  <table border="1" data-bbox="448 1272 1318 1442"> <thead> <tr> <th>z : Chip thickness</th> <th>y : Chip width</th> <th>x : Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed $1/3k$</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is the total length of each chip.</p> <p>6.1.2 Corner crack :</p>  <table border="1" data-bbox="448 1644 1318 1814"> <thead> <tr> <th>z : Chip thickness</th> <th>y : Chip width</th> <th>x : Chip length</th> </tr> </thead> <tbody> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed $1/3k$</td> <td>$x \leq 1/8a$</td> </tr> </tbody> </table> <p>⊙ If there are 2 or more chips, x is the total length of each chip.</p>	z : Chip thickness	y : Chip width	x : Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed $1/3k$	$x \leq 1/8a$	z : Chip thickness	y : Chip width	x : Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed $1/3k$	$x \leq 1/8a$	2.5
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06	Glass crack	<p>Symbols :</p> <p>x : Chip length y : Chip width z : Chip thickness k : Seal width t : Glass thickness a : LCD side length L : Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="469 831 1305 949"> <tr> <td>y : Chip width</td> <td>x : Chip length</td> <td>z : Chip thickness</td> </tr> <tr> <td>$y \leq 0.5 \text{ mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>6.2.2 Non-conductive portion :</p>  <table border="1" data-bbox="469 1290 1305 1397"> <tr> <td>y : Chip width</td> <td>x : Chip length</td> <td>z : Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>⊙ If the product will be heat sealed by the customer, the alignment mark must not be damaged.</p> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table border="1" data-bbox="783 1720 1305 1827"> <tr> <td>y : width</td> <td>x : length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$x \leq a$</td> </tr> </table>	y : Chip width	x : Chip length	z : Chip thickness	$y \leq 0.5 \text{ mm}$	$x \leq 1/8a$	$0 < z \leq t$	y : Chip width	x : Chip length	z : Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y : width	x : length	$y \leq 1/3L$	$x \leq a$	2.5
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NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit.	0.65
		8.2 Spots or scratches that appear when lit must be judged using LCD spot, lines and contamination standards.	2.5
		8.3 Backlight doesn't light or color is wrong.	0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.	2.5
		9.2 Bezel must comply with job specifications.	0.65
10	PCB \ COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination.	2.5
		10.2 COB seal surface may not have pinholes through to the IC.	2.5
		10.3 The height of the COB should not exceed the height indicated in the assembly diagram.	0.65
		10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.	2.5
		10.5 No oxidation or contamination PCB terminals.	2.5
		10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.	0.65
		10.7 The jumper on the PCB should conform to the product characteristic chart.	0.65
		10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hole pad, make sure it is smoothed down.	2.5
11	Soldering	11.1 No unmelted solder paste may be present on the PCB.	2.5
		11.2 No cold solder joints, missing solder connections, oxidation or icicle.	2.5
		11.3 No residue or solder balls on PCB.	2.5
		11.4 No short circuits in components on PCB.	0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet .	0.65
		12.12 The appearance of Heat Seal should not admit any dirt and break.	

4.2 Standard Specification for Reliability

4.2-1. Standard Specifications for Reliability of LCD Module

No	Item	Description
01	High temperature operation	The sample should be allowed to stand at 50 °C for 240 (-0, +48) hours under driving condition.
02	Low temperature operation	The sample should be allowed to stand at 0 °C for 240 (-0, +48) hours under driving condition.
03	High temperature resistance	The sample should be allowed to stand at 70 °C for 240 (-0,+48) hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 30 minutes.
04	Low temperature resistance	The sample should be allowed to stand at -20 °C for 240 (-0,+48) hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 24 hours.
05	Moisture resistance	The sample should be allowed to stand at 40 °C, 90 % RH MAX for 240 (-0,+48) hours under no-load condition excluding the polarizer, then taking it out and drying it at normal temperature.
06	Thermal shock resistance	The sample should be allowed to stand the following 10 cycles of operation: -40°C for 30 minutes → normal temperature for 5 minutes → +80°C for 30 minutes → normal temperature for 5 minutes , as one cycle.

4.2-2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 4.2, Standard specifications for Reliability have been executed in order to ensure stability.

NO	Item	Test Model	Inspection Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

4.2-3. Life Time

Life time	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($25 \pm 10^{\circ}\text{C}$), normal humidity ($45 \pm 20\% \text{ RH}$), and in area not exposed to direct sun light. (Life time of backlight, please refer to Data about backlight.)
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Note: From our experience the life time of high humidity operation and high temperature operation as above mentioned could be achieved.

4.3 Precautions in Use of LCM

4.3-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.

4.3-2 Storage

- Store in an ambient temperature of 5°C to 45°C , and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

4.3-3 Soldering

- Use the high quality solder. (60-63% tin mixed with lead)
- Iron: no higher than 260°C and less than 3-4 sec during soldering.
- Soldering: only to the I/O terminals.
- Rewiring: no more than 3 times.

4.3-4 Assembly

- The front polarizer is covered with a protective foil which should be removed before use.