

Table 4: ISD1000A and ISD1400 Address Space

Dec.	Binary								ISD1016A ISD1416 (Seconds)	ISD1020A ISD1420 (Seconds)
	A7	A6	A5	A4	A3	A2	A1	A0		
0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	1	0.1	0.125
8	0	0	0	0	1	0	0	0	0.8	1.0
10	0	0	0	0	1	0	1	0	1.0	1.25
13	0	0	0	0	1	0	0	0	1.3	1.625
50	0	0	1	1	0	0	1	0	5.0	6.25
100	0	1	1	0	0	1	0	0	10.0	12.5
159	1	0	0	1	1	1	1	1	15.9	19.875

160	1	0	1	0	0	0	0	0
through	Unused address space. An ISD1000A/1400 device addressed in this region will default to an overflow condition.							
191	1	0	1	1	1	1	1	1

192	1	1	0	0	0	0	0	0
through	This address space used by the ISD1000A/1400 Operational Modes.							
255	1	1	1	1	1	1	1	1