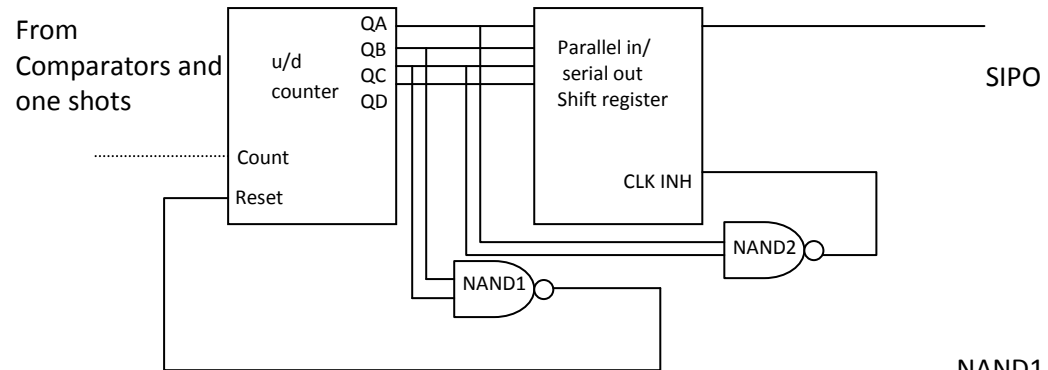


Transmitting the number 5



NAND1 only allows the counter to count up to 5 then resets back to 0.
NAND2 prevents numbers <5 to pass through the serial port of the shift register.

